

CIL
EMU CRITICAL ITEMS LIST

12/26/91 SUPERSEDES 08/31/90

Paper 8
Date: 12/12/91

NAME	P/M	FAILURE MODE & CAUSES	FAILURE EFFECT	RATIONALE FOR ACCEPTANCE
CAUTION AND WARNING SYSTEM, STEM 150 SV78597D-13 (1)	2/2	150FM03: BIT failure in A/D converter.	END ITEM: One or more BIT's in the 10 BIT word never change state. CAUSE: Electronic component failure.	A. Design - Established reliability capacitors and resistors are qualified to the applicable military specification and thermal shocked per Condition B Test Method 107 of MIL-STD-202. Microcircuits are qualified to the requirements of MIL-A-39510 and receive the burn-in of Class B parts per Method 5006 of MIL-STD-883. Transistors, diodes are qualified to the requirements of MIL-S-19540 and receive the burn-in of JANTRV Level parts per the applicable methods, 103B, 103P, 106G of MIL-STD-750. The electronic components are operating within the power derating requirements of SWS 7804. The printed circuit (PC) boards are fiberglass/epoxy per MIL-P-13959 type DP and manufactured in accordance with MSFC-STD-154. Parts mounting and soldering is per MSFC-STD-156 and MSFC300.4 (3A-1). The CWS is a mother/daughter board assembly. The daughter boards are held in place by metal card guides which also provide thermal transfer from the board heat sinks to the CWS case. The top cover of the CWS exerts a downward force on the daughter boards to keep them properly seated in the mother board connectors. Flex tape (Kapton insulated, flexible flat conductor) instead of conventional teflon coated wires is used to provide connections between the mother board and the external connectors. This prevents pinching of the conductor during item assembly. The PC board assemblies are conformal coated per MIL-A-46146 (Bow Corning RTV 3140) for environmental and humidity protection. Electrical connectors are environmentally sealed to prevent damage due to contamination and humidity. B. Test - Component Acceptance Test - Full functioning of the CWS is verified during Item AIP Tests. These include continuity, logic flow, n-state and fault messages, warning and alert tones activation, and BITE activation. These tests are conducted upon completion of random vibration testing. PQA Test - The above electrical tests are repeated during PQA to

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2/2	150FM05		

verify CMS operation. The CMS is also operational during other PLSS PDA electrical tests such as sensor accuracy checks, Item 123 Fan operation, Item 174 8105 checkout, and solenoid valve actuation.

C. Certification Test -

The item completed the 15 year structural vibration and shock certification requirements during 10/89. EC's 42806-246 (add jumper wires, add diode CR221, change resistor R301), 42886-365-3 (eliminate interferences with PLSS), 42806-710 (overstressed resistor R305 due to delta data logger, software change, diode VR201 resulting) 42806-942 and 42886-952-1 (transistor Q281 lead stress relief) have been incorporated and certified by similarity or analysis since this configuration was tested.

D. Inspection -

Each circuit board, the flex tape, and connectors are inspected for damage and contamination prior to being placed into finished stores. The CMS assembly is inspected internally and externally for damage and contamination during item assembly and externally during ATP. All soldering is inspected by HS QA and DCAS QA per NH85300.4 (3A-1).

E. Failure History -

None.

F. Ground Turnaround -

Tested per FEMU-R-801, DCM bite tight verification during vacuum chamber run.

G. Operational Use -

Crew Response - Trouble shoot problem, if no success, consider EMU 3 if available. If memory can be determined to be x-state failure, no constraint, continue EVA. Otherwise EMU go for SOU ops.

EVA: When CMS issues erroneous messages, troubleshoot with RMS, continue EVA.

Training - No training specifically covers this failure mode.

P&G 1051
SMDU-44-001

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ANALYST:

NAME	FAILURE	MODE & CAUSES	FAILURE EFFECT	RATIONALE FOR ACCEPTANCE
2/2	150FM03:			Operational considerations - Flight rules define operational CRS as at least able to monitor a valid status list. EVA checklist procedures verify hardware integrity and systems operational status prior to EVA. Real Time Data System allows ground monitoring of EMU systems.